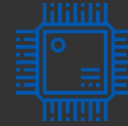


# **AM62L EVM**

## **[PROC181E1-1]**

### **Power Sequence**

Date: 26 February 2025



# **AM62L EVM**

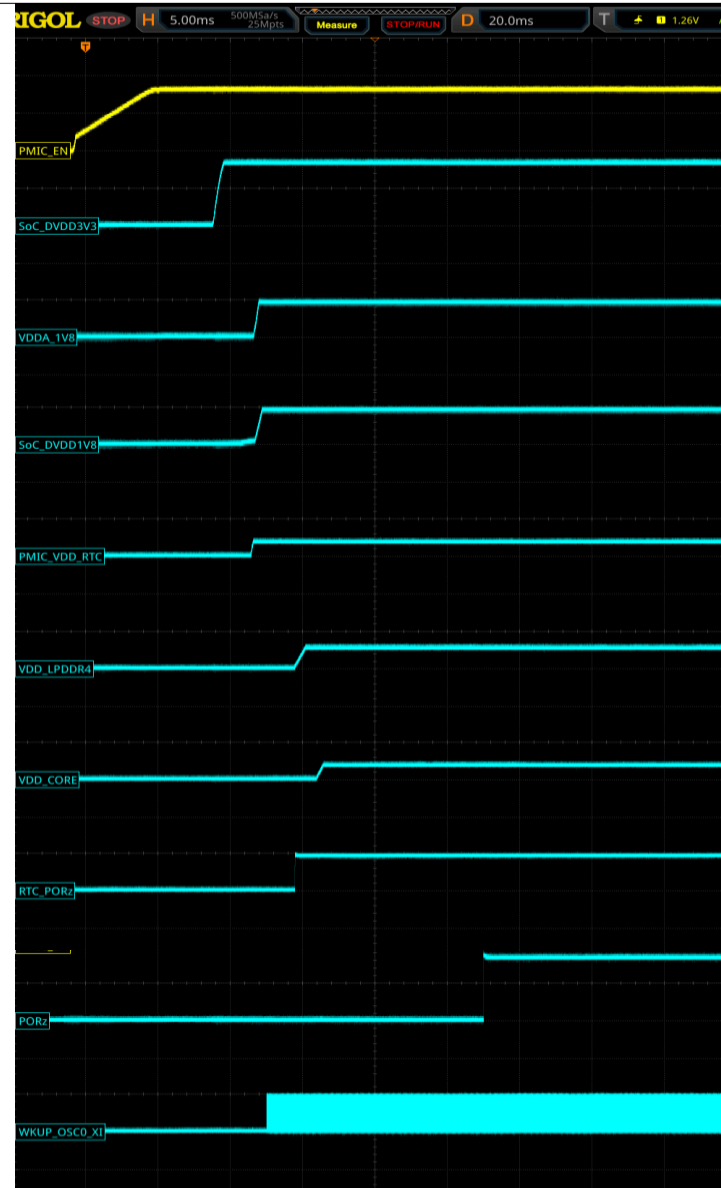
## **REV E1-1**

### **Power Up Sequence**

# RTC + IO + DDR Mode - Power up sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (ms)
PMIC_EN	3.3	0
SoC_DVDD3V3	3.3	9.6
VDDA_1V8	1.8	12
SoC_DVDD1V8	1.8	12.2
PMIC_VDD_RTC	0.75	11.6
VDD_LPDDR4	1.1	15.2
VDD_CORE	0.75	16.4
RTC_PORz (Reset)	1.8	14.5
PORz (Reset)	3.3	27.55
WKUP_OSC0_XI	1.8	12.55

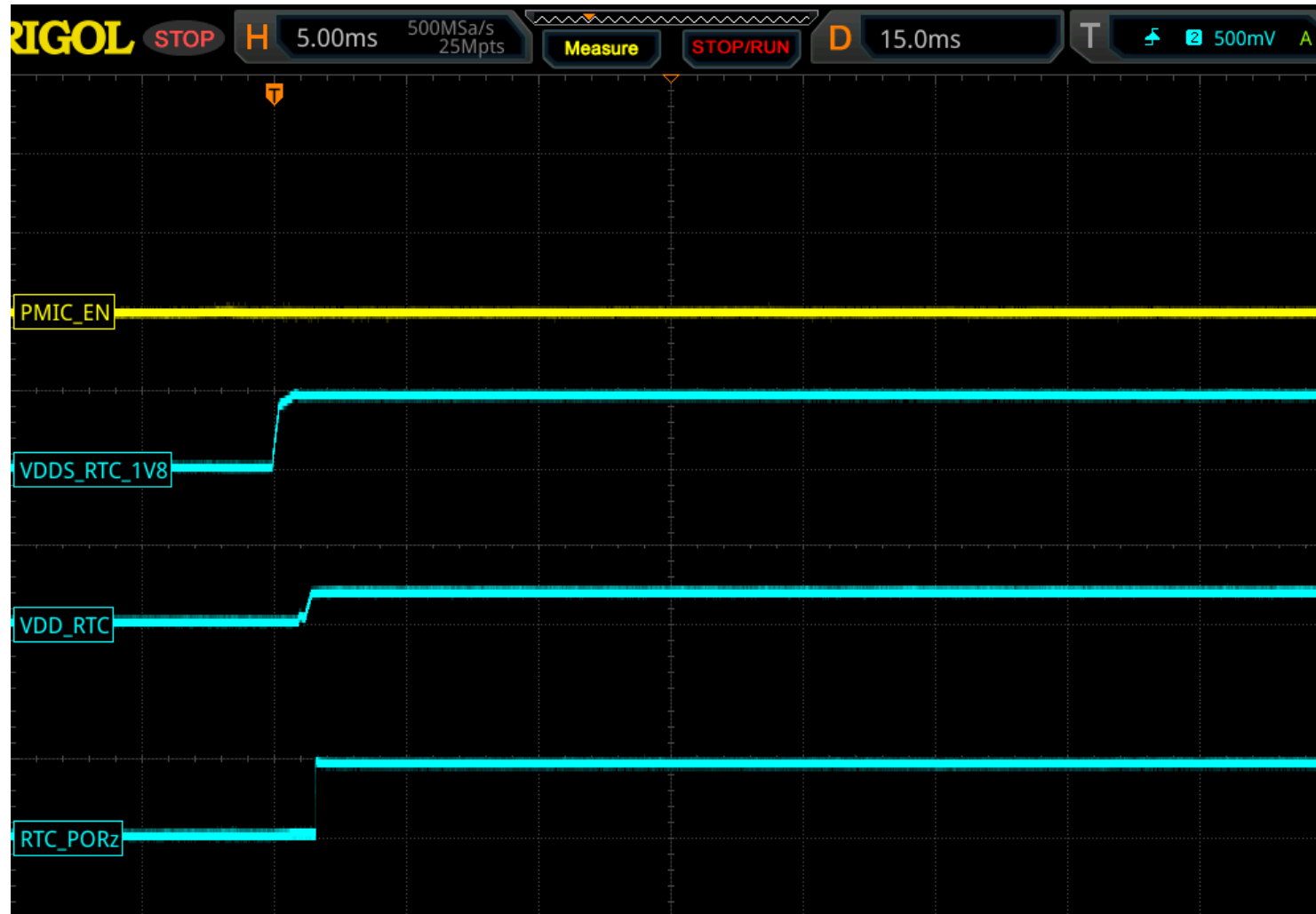
# RTC + IO + DDR Mode - Power up sequence



# RTC Only Mode - Power up sequence timing measurements

	Voltage (V)	Elapsed Time (ms)
VDDS_RTC_1V8	1.8	0
VDD_RTC	0.75	0.7
RTC_PORz (Reset)	1.8	0.9

# RTC Only Mode - Power up sequence



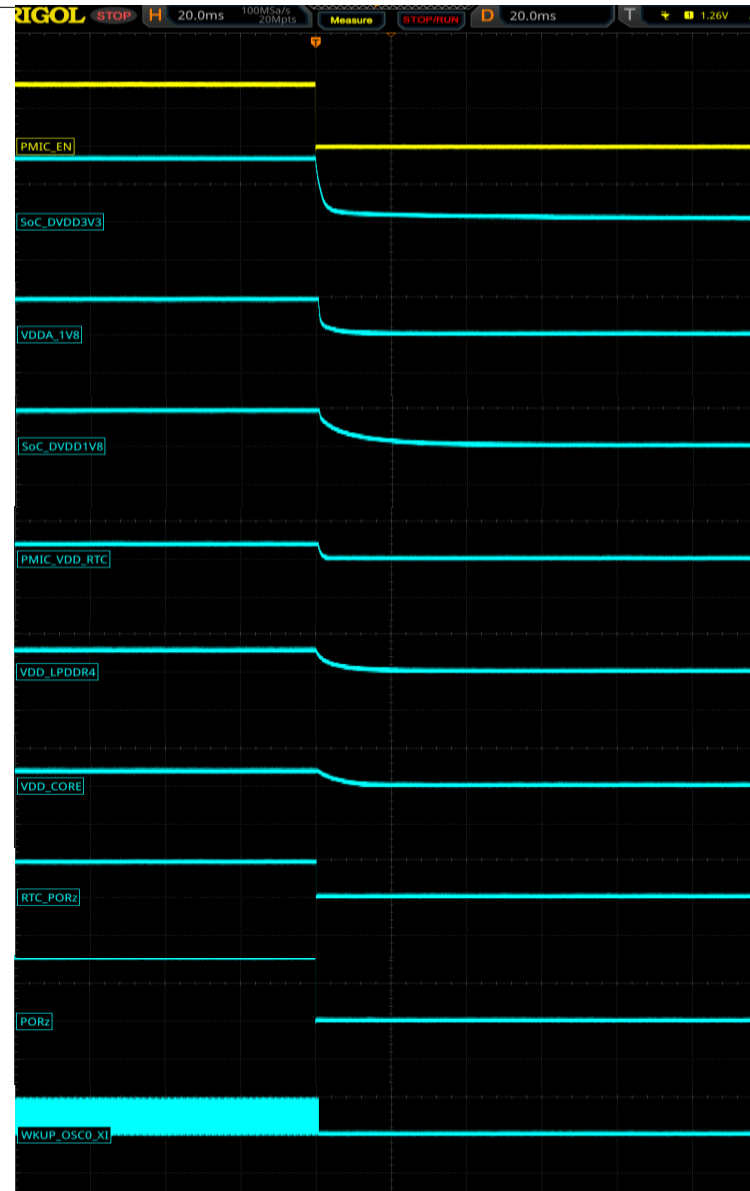
# **AM62L EVM REV E1-1 Power Down Sequence (Uncontrolled)**

# RTC + IO + DDR Mode - Power down sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (ms)
PMIC_EN	3.3	0
SoC_DVDD3V3	3.3	60
VDDA_1V8	1.8	13
SoC_DVDD1V8	1.8	40.2
PMIC_VDD_RTC	0.75	2.6
VDD_LPDDR4	1.1	19.2
VDD_CORE	0.75	12.8
RTC_PORz (Reset)	1.8	0
PORz (Reset)	3.3	0
WKUP_OSC0_XI	1.8	0.8



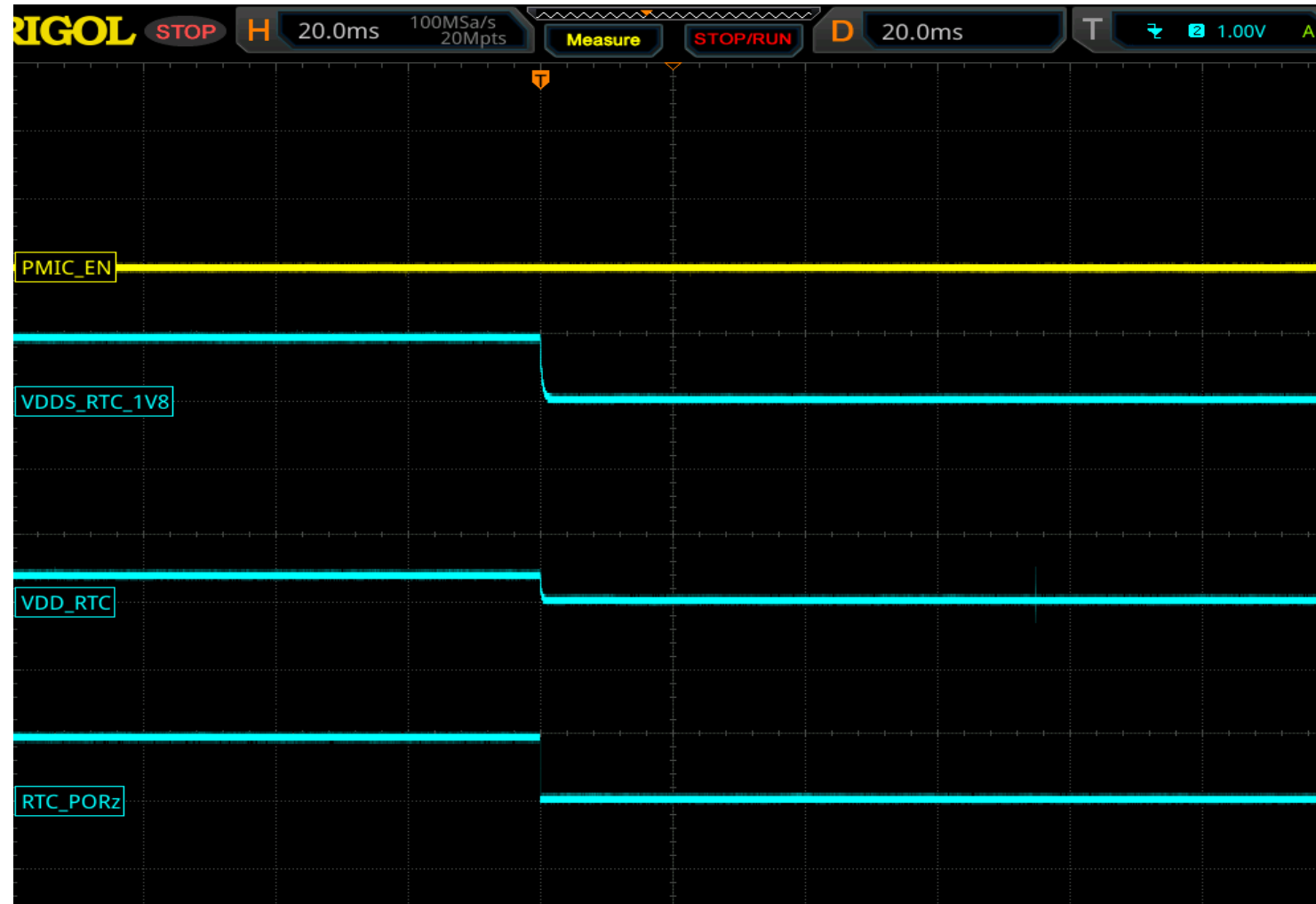
# RTC + IO + DDR Mode - Power down sequence



# RTC Only Mode – Power down sequence timing measurements

Power Supply	Voltage (V)	Elapsed Time (ms)
VDDS_RTC_1V8	1.8	1.2
VDD_RTC	0.75	0.6
RTC_PORz (Reset)	1.8	0

# RTC Only Mode – Power down sequence





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